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Write – Many Memory Device
With Sideband Fields

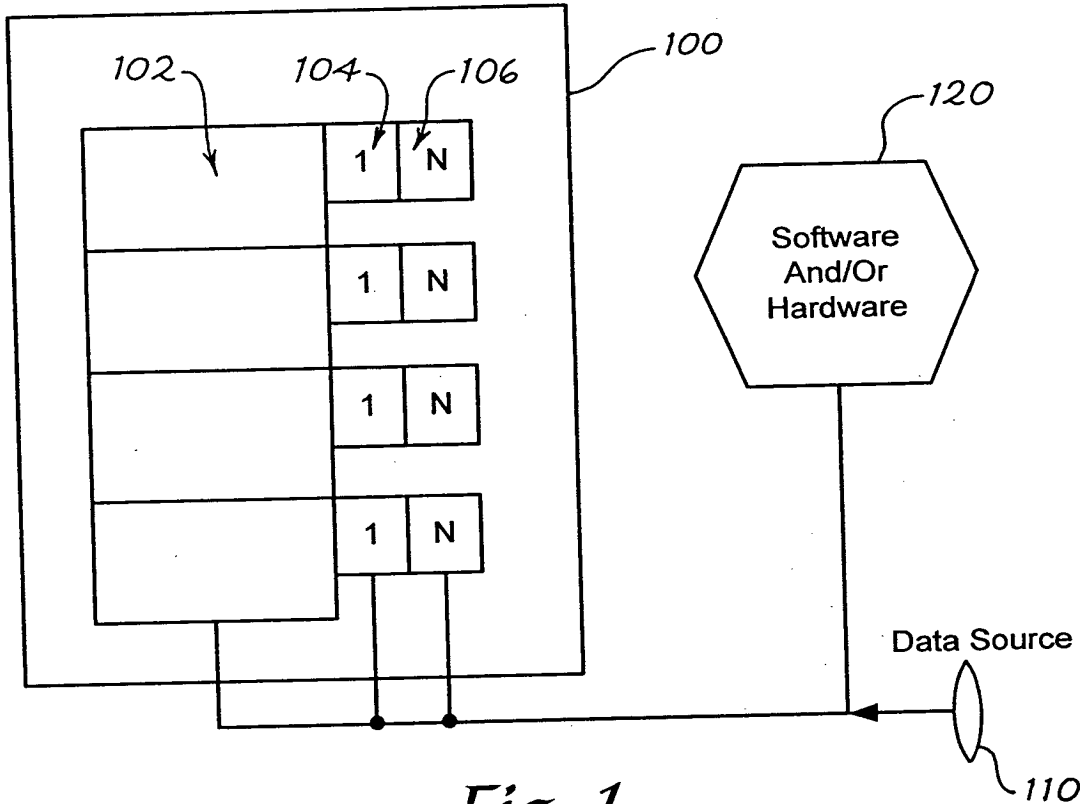


Fig. 1

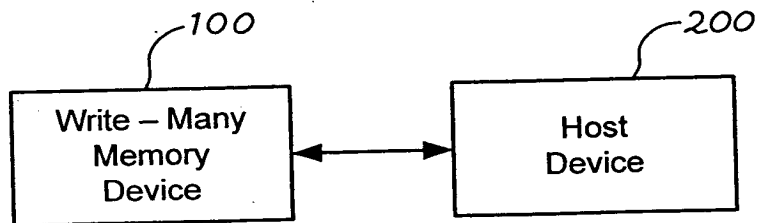
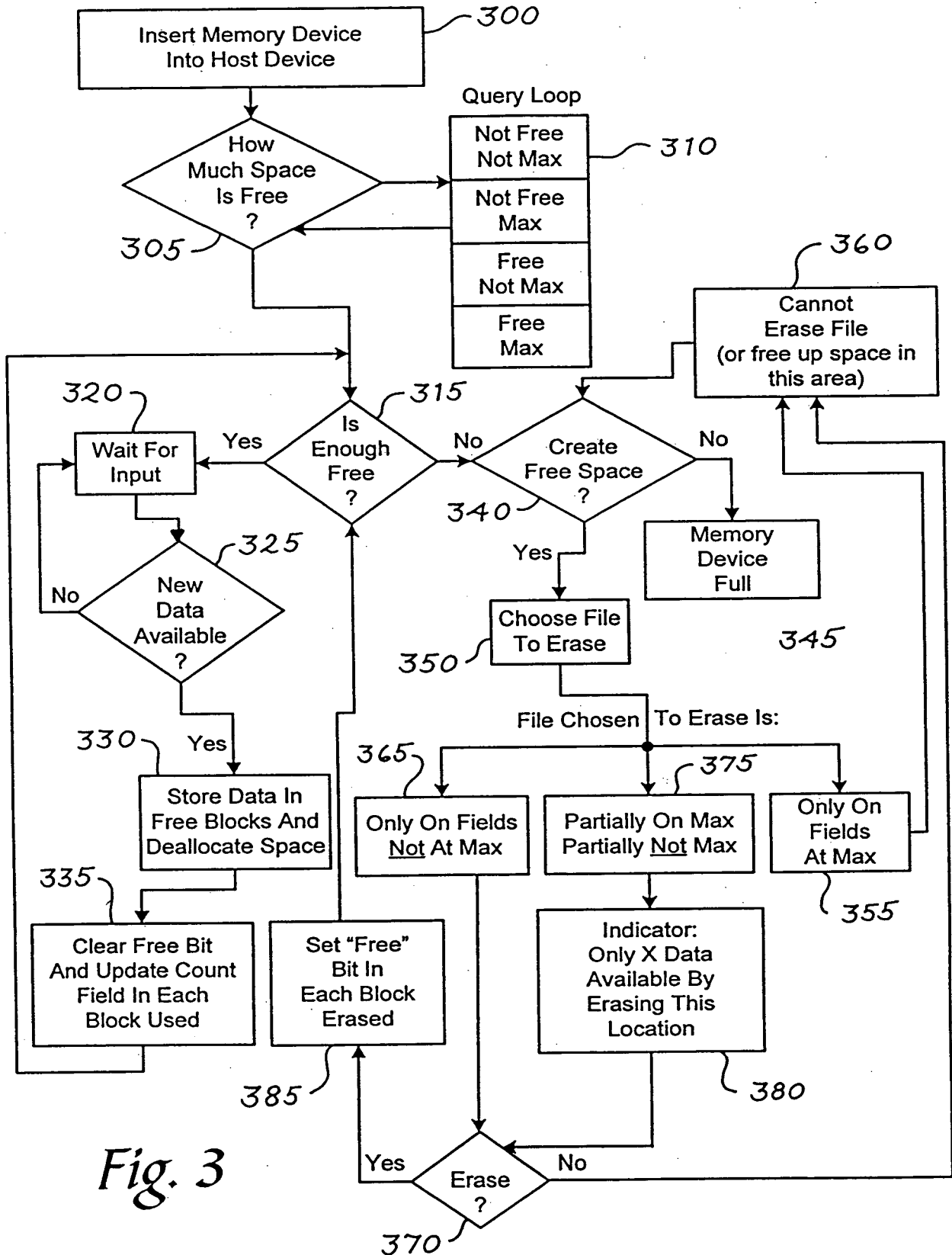


Fig. 2



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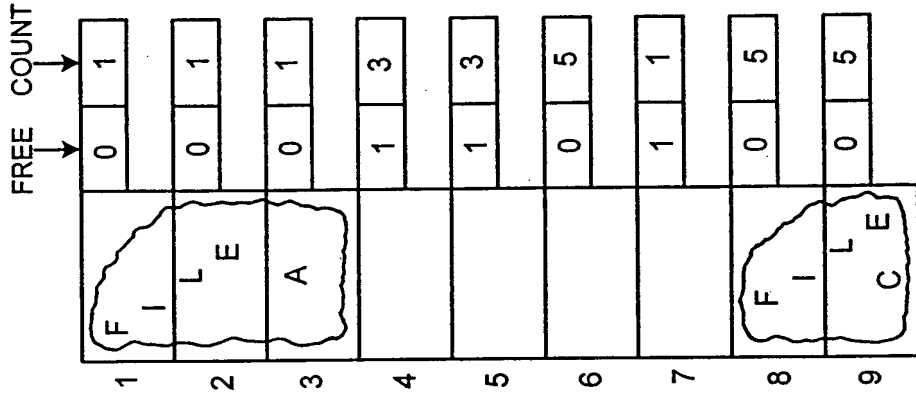


Fig. 4A

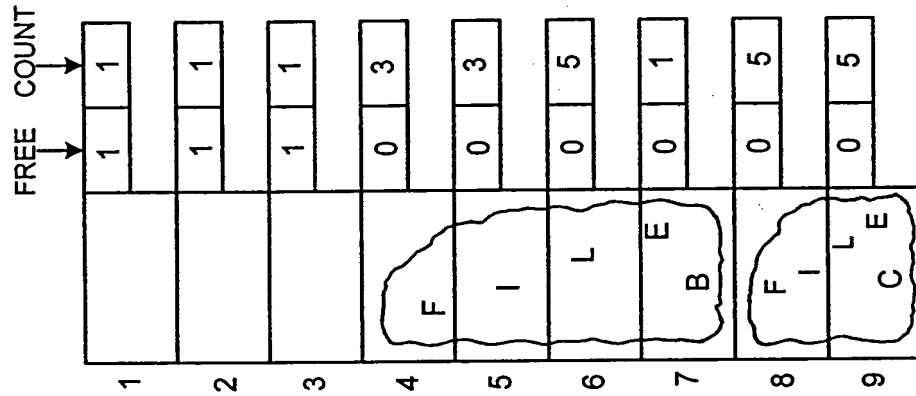


Fig. 4B

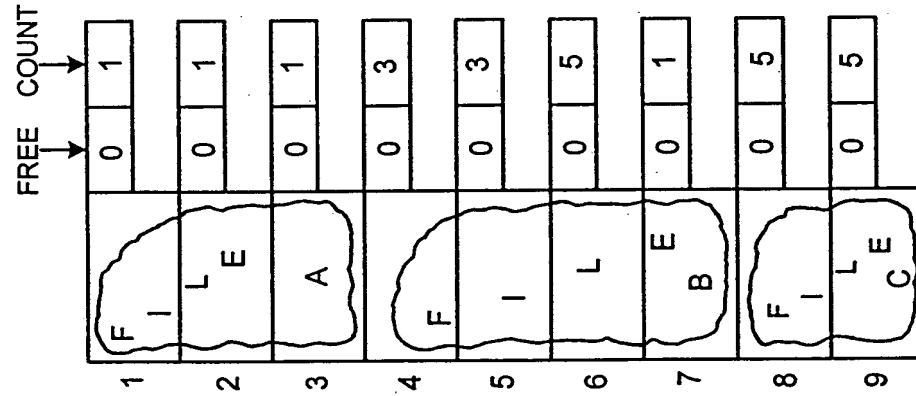


Fig. 4C

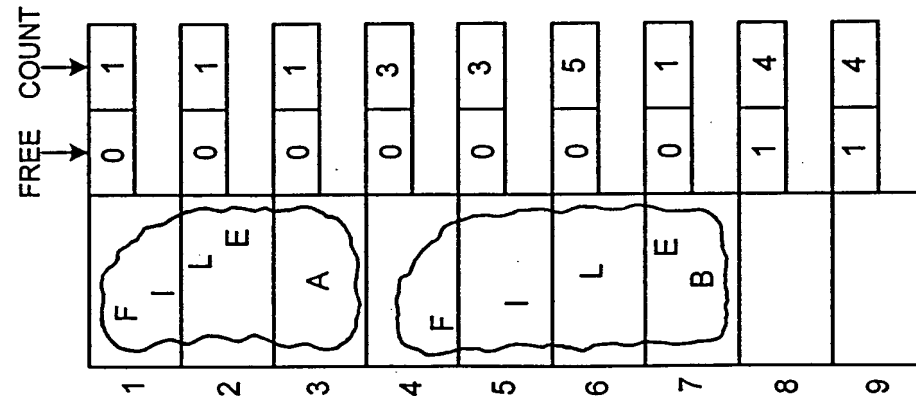


Fig. 4D